Architecture for high-sensitivity single-shot readout and control of the electron spin of individual donors in silicon

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We describe a method to control and detect in single shot the electron spin state of an individual donor in silicon with greatly enhanced sensitivity. A silicon-based single-electron transistor (SET) allows for spindependent tunneling of the donor electron directly into the SET island during the readout phase. Simulations show that the charge transfer signals are typically $\Delta q \gtrsim 0.2e$ —over an order of magnitude larger than achievable with metallic SETs on the SiO₂ surface. A complete spin-based qubit structure is obtained by adding a local electron spin resonance line for coherent spin control. This architecture is ideally suited to demonstrate and study the coherent properties of donor electron spins, but can be expanded and integrated with classical control electronics in the context of scale up.

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Electron spins bound to donor nuclei in silicon have exceptionally long coherence and relaxation times, relative to the time scale for the control of their quantum state, $¹$ and are</sup> thus a promising qubit system. The electron spin coherence time of a phosphorus donor is $T_2>60$ ms at $T=6.9$ K in isotopically pure 28 Si,² already orders of magnitude longer than for GaAs quantum dot systems $3,4$ $3,4$ and still far from the theoretical limit (dominated by the ²⁹Si impurity fraction,⁵ which can be minimized through processing). However, a major obstacle to realizing a donor electron spin qubit in silicon 6 is the difficulty of detecting the spin state for individual donors typically $10-20$ nm below a $SiO₂$ interface.

The first proposals for donor spin readout involved spin-to-charge conversion^{7,[8](#page-3-9)} through spin-dependent tunneling to a doubly occupied D[−] donor state. The change in electrostatic potential caused by an electron can be detected by a single-electron transistor (SET) on the $SiO₂$ surface, as shown, e.g., in a double-donor well structure.⁹ The sensitivity of the detection scheme is quantified by the charge transfer signal $\Delta q/e$, defined as the relative shift in the SET bias point caused by the displacement of a nearby charge. $\Delta q/e$ =1 if an electron is removed from the SET island and taken to infinity. Clearly, $\Delta q/e < 1$ if the coupling between donor and SET is purely electrostatic, i.e., no charge can be directly transferred between the two. The exact value depends on how far the electron can move to/from or in the vicinity of the SET island: for a charge moving some 20 nm laterally in the silicon, 20 nm below the tip of the island of a surface SET, the signal is typically very small, $\Delta q/e \sim 0.01$. This fact has encouraged proposals in which $\Delta q/e$ can be increased by confining the donor electron close to the $SiO₂$ interface, $\frac{10}{10}$ thus closer to the SET. On the other hand, in quantum dot systems the successful readout of a single electron spin has been achieved by monitoring the spindependent tunneling of the electron into a reservoir.¹¹ Here we take this concept a step further and present a donorbased, electron spin qubit device, where the charge transfer signal upon spin readout can be increased by over an order of magnitude as compared to previous proposals. Our architecture combines recently developed silicon quantum dot^{12} and SET (Ref. [13](#page-3-14)) technologies with precise single-ion implantation^{1[4](#page-3-5)} and local electron spin resonance (ESR).^{4[,15](#page-3-16)} The crucial feature of this device consists in using the island of a subsurface silicon SET as the electron reservoir for spindependent electron tunneling, greatly enhancing the charge signal. We show that this method allows fast (potentially in the nanosecond range) and high-sensitivity spin readout with no back action before the measurement, therefore protecting the qubit from decoherence due to the measurement setup.

A sketch of the proposed donor spin qubit device is shown in Fig. $1(a)$ $1(a)$. It consists of three main elements: (i) a phosphorus donor, introduced in the intrinsic Si substrate by single-ion implantation;¹⁴ (ii) a Si-SET,^{12[,13](#page-3-14)} fabricated next to the donor implant site, with a distance $d \sim 50$ nm between donor and SET island; and (iii) a coplanar transmission line, terminated by a short-circuit that runs just above the donor. The Si-SET comprises two aluminum barrier gates and an overlaying top gate, insulated by Al_2O_3 and deposited on high-quality $SiO₂$, typically 5 nm thick. The top gate extends to source/drain n⁺ doped regions, and induces an electron layer under the SiO_2 when biased to $V_{top} \gtrsim 1$ V. Setting the barrier gate voltages to $V_{B1,2} < V_{top}$, the bottom of the conduction band can be lifted above the Fermi energy E_F , thereby interrupting the electron layer and forming the island of a SET [Fig. $1(b)$ $1(b)$].

The crucial difference between the Si-SET described here and the more common surface Al-SETs is that the SET island now consists of a small area of electron gas, electrostatically induced *under the* $SiO₂ layer$. Therefore, it is possible to construct a device where *an electron can tunnel between the donor and the island of the SET*. This feature gives a charge transfer signal an order of magnitude larger than achievable with other detection schemes, where the charge sensor is only electrostatically coupled to the qubit. In addition, the Si-SET has the advantage of being compatible with standard metal-oxide-semiconductor (MOS) fabrication processes

FIG. 1. (Color online) (a) Schematic top view of the spin qubit device. The donor-SET island distance is $d \sim 50$ nm. (b) Energy profile along the dash-dotted line in panel (a). The density of electron states in the SET island is approximated as a quasicontinuum. (c) In reality, the SET has a finite number of electrons: the quasicontinuum approximation and the spin readout method are valid as long as $\Delta E \ll E_Z$.

since it does not require shadow metal evaporation.

The shorted coplanar transmission line serves a double purpose: (i) it carries microwave pulses that produce an oscillating magnetic field, B_1 , for local ESR,⁴ and (ii) it acts as a dc gate for the electrostatic potential of the donor below it through application of a static voltage V_D . The ability to use a transmission line to perform local ESR of donor spins while simultaneously applying a dc bias was recently demonstrated by electrically detected magnetic resonance at *T* ≤ 1 K.^{[15](#page-3-16)} Since the transmission line is nonresonant, the structure is entirely broad band. The purpose of the shortcircuit termination is to create a node of the electric field at the donor site, while having the maximum value of the current, i_{ESR} , for fast coherent manipulation of the spin state.⁴ If the short-circuit termination has a cross-section \sim 100 \times 100 nm², a Rabi π pulse can be obtained in <100 ns with less than −30 dBm microwave power on the chip.

Figure $1(b)$ $1(b)$ shows the energy profile along the dashdotted line in Fig. $1(a)$ $1(a)$. First we encounter the potential well created by the donor ion, which can bind one $(D⁰)$ or two (D⁻) electrons. In the following we shall restrict our analysis to the one-electron D^0 state. By applying a static magnetic field B_0 in the plane of the chip, the energy of the $|\uparrow\rangle$ and $|\downarrow\rangle$ spin states is split by an amount $E_Z = 2g\mu_B B_0 S$, with $g \approx 2$ and *S*=1/2. Proceeding further we enter the SET island, where the bottom of the conduction band E_C is pushed below the Fermi level E_F by the positive V_{top} . Between donor and SET is an energy barrier that allows electron tunneling at a rate Γ_{D} , mainly determined by the distance d (~50 nm) between donor and island. Turning toward the drain contact, we cross the tunnel barrier created by $V_{B2} < V_{top}$. This barrier is easily tunable to have a tunnel rate $\Gamma_{\text{SET}} \gg \Gamma_{\text{D}}$.

Spin-to-charge conversion is achieved by tuning

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 ${V_D, V_{top}}$ so that only the energy of the $|\uparrow\rangle$ state is above the electrochemical potential of a charge reservoir. The detection of a charge transfer from donor to reservoir then corresponds to the single-shot projective measurement of the $|\uparrow\rangle$ state, as demonstrated in $\tilde{G}a\tilde{A}s$ quantum dots.¹¹ Here we propose to use the SET island as electron reservoir, instead of having a separate bulk electron layer. For this readout method to work, the Fermi distribution in the SET island must be sharp on the energy scale set by E_z . This condition is abundantly fulfilled by cooling the system to $T \sim 100$ mK and using an operation frequency \sim 40 GHz for the ESR system, corresponding to $\bar{B_0} \approx 1.4$ T and $E_Z/k_B \approx 2$ K. In fact, due to the finite number of electrons, the density of states in the SET island is not a real continuum, but for the purpose of spin readout it is sufficient for the single-particle energy spacing, ΔE , to be much smaller than E_Z and comparable to $k_B T$ [Fig. [1](#page-1-0)(c)]. Taking an SET island of area $A = 50 \times 100$ nm², we estimate $\Delta E \sim 2 \pi \hbar^2 / g m^* A = 24 \mu eV$ (g=4 is the spin +valley degeneracy and $m^* = 8.9 \times 10^{-31}$ kg is the effective mass), already smaller than the thermal broadening of the Fermi function at $T \sim 100$ mK.

The advantage of having an integrated charge sensor and electron reservoir is best appreciated by thinking of this architecture as a "parallel double quantum dot" configuration.¹⁶ We label the donor as "dot 1," which can only have 0 or 1 electrons, and the SET island as "dot 2," with a large number of electrons *N*,*N*+1,... and charging energy $E_{C2} \sim 1$ meV.¹³ The charging energy of the donor is $E_{C1} \sim 30-40$ meV, depending on the capacitance to its surroundings.¹⁷ Dot 2 is connected to source and drain contacts and can be measured in transport, while the charge state of dot 1 can only be changed by electron tunneling to or from dot 2.

For two quantum dots with mutual coupling energy E_{m} , the electrochemical potential of one dot depends on the charge state of the other.¹⁸ Therefore the SET can have two ladders of electrochemical potentials, shifted by *E*^m with respect to each other, depending on the charge state of the donor. The donor states are split by $E_Z = \mu_1(1_\uparrow, N)$ $-\mu_1(1_\downarrow, N)$. In Fig. [2](#page-2-0)(a) we sketch a situation where the charge state of the "double dot" is $(1, N)$ (the donor is neutral), thus the relevant electrochemical potentials on the SET side are the solid lines, $\mu_2(1,N), \mu_2(1,N+1), \dots$ and the corresponding $\{V_D, V_{top}\}$ point in Fig. [2](#page-2-0)(b) is given by the dashes labeled $|\downarrow\rangle$, $|\uparrow\rangle$. The SET is thus in Coulomb blockade, with $I_{\text{SET}} = 0$, because $\mu_2(1, N+1)$ is far above the Fermi level in the source/drain contacts.

Next we wish to lift μ_1 by decreasing V_D , while keeping the μ_2 ladder fixed and $\mu_2(0, N+1)$ in the source/drain bias window. Because of cross capacitance, this requires a compensating V_{top} to keep the operation point $\{V_{\text{D}}, V_{\text{top}}\}$ along the solid line in Fig. [2](#page-2-0)(b). When $\mu_1(1, N) > \mu_2(0, N+1)$, the donor electron can tunnel onto the SET island (at a rate Γ_{D}) and out to the drain (at a rate $\Gamma_{\text{SET}} \gg \Gamma_{\text{D}}$), leaving behind a positive charge at the donor site. This charge "pulls down" the ladder of electrochemical potentials for the SET, now repre-sented by the dashed lines in Fig. [2](#page-2-0)(a), $\mu_2(0, N)$, $\mu_2(0, N)$ $+1$,... and since $\mu_2(0, N+1)$ is in the source-drain bias window, *I*_{SET} jumps to its maximum value. The charge transfer signal is $\Delta q/e = \Delta V_{\rm m}/\Delta V_{\rm C2}$ $\Delta q/e = \Delta V_{\rm m}/\Delta V_{\rm C2}$ $\Delta q/e = \Delta V_{\rm m}/\Delta V_{\rm C2}$ [Fig. 2(b)], i.e., the shift of the

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FIG. 2. (Color online) (a) Sketch of the electrochemical potentials in the "parallel double dot" picture. The relevant SET potentials are the solid lines when the donor is neutral $(D^0$ state) or the dashed lines when the donor is ionized $(D^+$ state). (b) Charge stability diagram in the double dot picture. The broad lines represent the SET current peaks, $I_{\text{SET}} > 0$, spaced by $\Delta V_{C2} = E_{C2} / e \alpha_2$ along the V_{top} axis, where α_2 is the lever arm between top gate and SET island. The dashes labeled $|\uparrow\rangle$, $|\downarrow\rangle$ represent the positions of $\mu_1(1_\uparrow, N)$ and $\mu_1(1_\downarrow, N)$ as given in panel (a). The μ_2 ladder can be kept fixed by moving $\{V_D, V_{top}\}$ along the thin black line.

SET Coulomb peaks due to the ionization of the donor, divided by their period. Note that $\Delta q/e$ is exactly what would be obtained by moving a positive charge from infinity to the donor site. Thus, $\Delta q/e \rightarrow 1$ as the donor location approaches the SET island.

We have set up a device model, shown in Figs. $3(a)$ $3(a)$ and $3(b)$ $3(b)$, for use in the boundary-element capacitance extraction code FASTCAP (Ref. [19](#page-3-20)) to determine coupling capacitances as a function of donor position. In the model the gates and electron layers are described by metallic conductors of the appropriate size and the donor is represented as a metal cube with sides of length $2a_B$ $(a_B \approx 2.5 \text{ nm})$ is the Bohr radius in silicon). $\Delta q/e$ can be expressed as the ratio $C_m/C_{1\Sigma}$, where *C*^m is the mutual capacitance between donor and SET island, and $C_{1\Sigma}$ is the total donor capacitance. Figure [3](#page-2-1)(c) shows the resulting $\Delta q/e$, assuming 50 nm gap between top gate and ESR line, 5 nm $SiO₂$ thickness, and donor in the *y*-*z* plane. For a donor right under the tip of the ESR line, and 15 nm below the Si/SiO₂ interface, we find $\Delta q/e$ ~ 0.2. With E_{C2} \sim 1 meV and $T \sim 100$ mK, I_{SET} can shift from zero to its maximum value, I_{max} .

A typical spin control and readout sequence would pro-

FIG. 4. (Color online) Pulsing protocol and corresponding SET signal for the coherent control and readout of a donor electron spin. The donor gate voltages $V_{1,2,3}$ $V_{1,2,3}$ $V_{1,2,3}$ are shown also in Fig. 2(b).

ceed as shown in Fig. [4,](#page-2-2) always assuming the μ_2 ladder is kept fixed by using compensated $\{V_D, V_{top}\}$ pulses. (i) Empty: the donor is ionized when $V_D = V_1$, causing $\mu_1(1_\downarrow, N)$ $>\mu_2(0, N+1)$. The successful donor ionization is signaled by $I_{\text{SET}} = I_{\text{max}}$. (ii) Load: a new electron is loaded into the ground Zeeman state $|\downarrow\rangle$, by choosing V_2 such that $\mu_1(1_\uparrow, N) > \mu_2(0, N+1) > \mu_1(1_\downarrow, N)$, and $I_{\text{SET}} = 0$. (iii) ESR pulse: when $V_D = V_3$ both donor levels are far below $\mu_2(0, N+1)$. The spin undergoes coherent Rabi rotations under the effect of microwave pulses applied to the ESR line. Here we take the example of a π -pulse where the final state is $|\uparrow\rangle$. (iv) Readout: $V_D = V_2$, and since $\mu_1(1_\uparrow, N) > \mu_2(0, N)$ +1), the electron in the $|\uparrow\rangle$ state tunnels off the donor into the SET island, unblocking the conduction. However, since $\mu_2(0, N+1) > \mu_1(1, N)$, another electron can tunnel onto the donor in the state $|\downarrow\rangle$, blocking the SET again. Thus, an electron in $|\uparrow\rangle$ is signaled by a "blip" in *I*_{SET}, with a duration of order Γ_{D}^{-1} . The inhomogeneous spin coherence time, T_2^* , can be extracted by observing the decay of Rabi oscillations, obtained by counting the occurrence of $|\uparrow\rangle$ states as a function of ESR pulse duration. The spin-lattice relaxation time, *T*1, can be obtained by loading an electron in an unknown state, i.e., using $V_D = V_3$ for the load pulse, then counting the occurrence of $|\uparrow\rangle$ states as a function of the waiting time between load and measurement 11 (no ESR required).

> FIG. 3. (Color online) (a) Top and (b) bottom view of the device model used to calculate the charge transfer signal $\Delta q/e$. Filled areas represent gates and electron layers as indicated. (c) FASTCAP calculation of $\Delta q/e$ as a function of donor position in the *y*-*z* plane. We used a mesh five times finer than shown by the thin lines in (a) and (b), yielding discretization errors $<$ 5% of the $\Delta q/e$ value at each point. For a donor under the tip of the donor gate, 15 nm below the Si/SiO₂ interface, $\Delta q/e \approx 0.2$ (black arrow).

An important feature of our device is that, due to Coulomb blockade, $I_{\text{SET}}=0$ for the whole time the electron resides on the donor, i.e., the charge sensor is automatically switched off. The type of back action arising, e.g., from the current through a quantum point contact used as charge sensor, 20 is therefore eliminated.

The time scale for projective readout of the qubit is set by the typical tunneling time between donor and SET island, $\Gamma_{\rm D}^{-1}$. In the design discussed here, $\Gamma_{\rm D}$ is exponentially sensitive to *d*, but some level of control could be achieved by introducing an additional gate between ESR line and SET. However we note that the acceptable range of Γ_{D} for reliable spin readout is extremely wide. The upper bound to Γ_{D}^{-1} is set by the spin-lattice relaxation time, T_1 , since an electron in the $|\uparrow\rangle$ state must be measured before it decays to $|\downarrow\rangle$. The bulk value for *P* in Si is $T_1 \approx 3000$ s at $T = 1.25$ K, further increasing as $1/T$.^{[21](#page-3-22)} For a donor placed near a $Si/SiO₂$ interface, paramagnetic defects and charge traps may give an additional contribution to the spin relaxation and dephasing. 22 However, this contribution vanishes exponentially when $k_B T \ll E_Z$ due to high spin polarization.²³ The lower bound to $\Gamma_{\rm D}^{-1}$ is simply set by the bandwidth of the circuit that detects I_{SET} . Here we note that an ideal readout scheme would exploit the fact that our system effectively provides a digital

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signal $(I_{\text{SET}} = 0 \text{ or } I_{\text{max}})$ by connecting the SET to a cryogenic current comparator.²⁴ This method could yield the ultimate readout speed, potentially as fast as \sim 1 ns. The MOS compatibility of our design is significant, because it allows the on-chip integration of qubit devices and ultrafast cryogenic readout electronics, using the same industry-standard fabrication process. This is extremely advantageous both for readout speed—minimizing the capacitance of the interconnects– and for the scale up of a quantum computer.

In conclusion, we have shown that recently developed $Si-SET$ technology^{12[,13](#page-3-14)} can be harnessed to create a compact donor spin qubit architecture, wherein the island of the SET is used as a reservoir for high-sensitivity spin readout. The compatibility with MOS fabrication techniques is very advantageous to integrate the qubits with digital on-chip electronics²⁴ and achieve the ultimate in readout speed and sensitivity for spins in the solid state. This architecture removes a major impediment to exploiting the natural advantages of donors in silicon as a platform for scalable qubit science.

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